

Figure 4-4. CPU and Toggle Switches

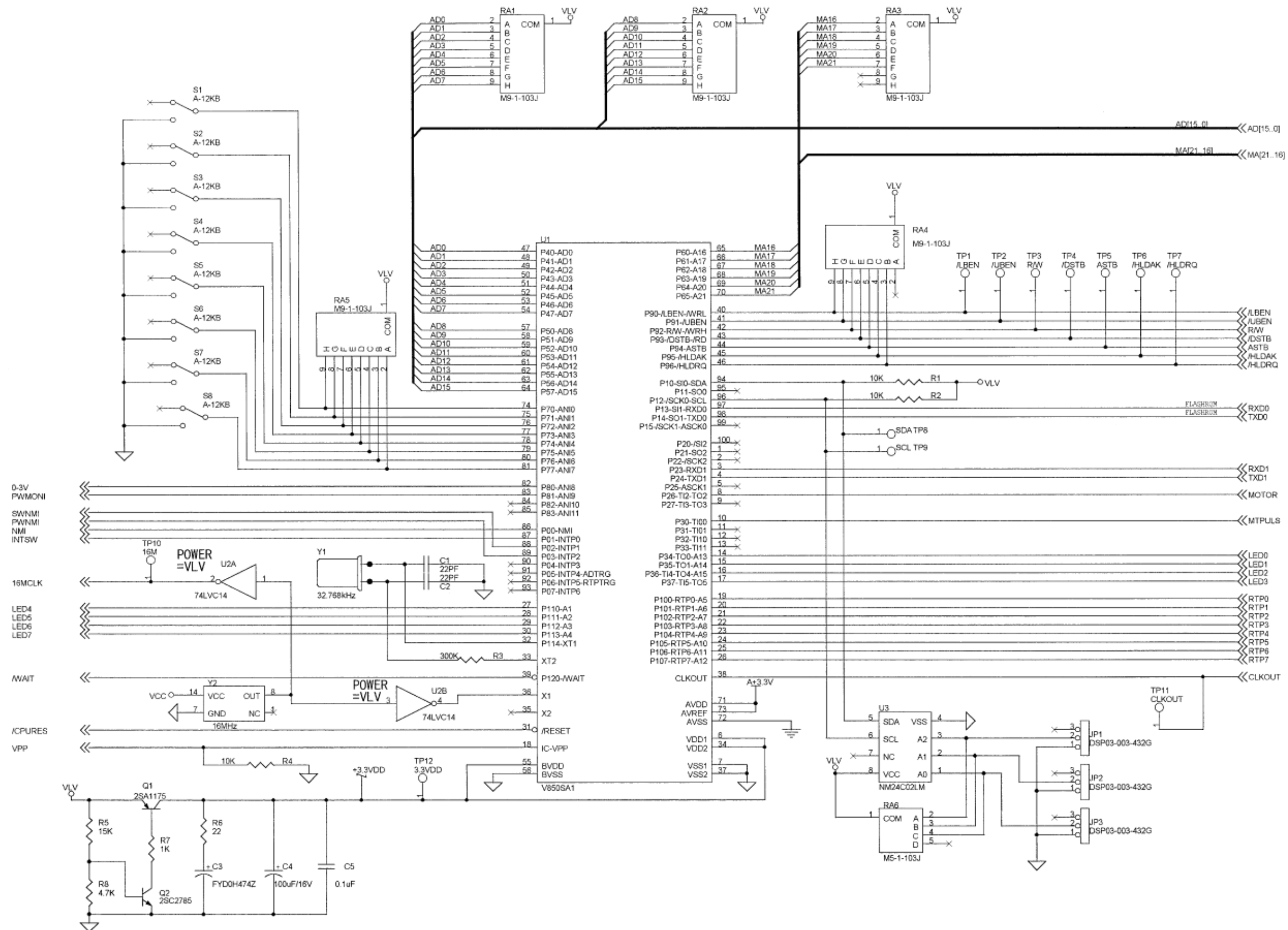


Figure 4-5. Device Controller

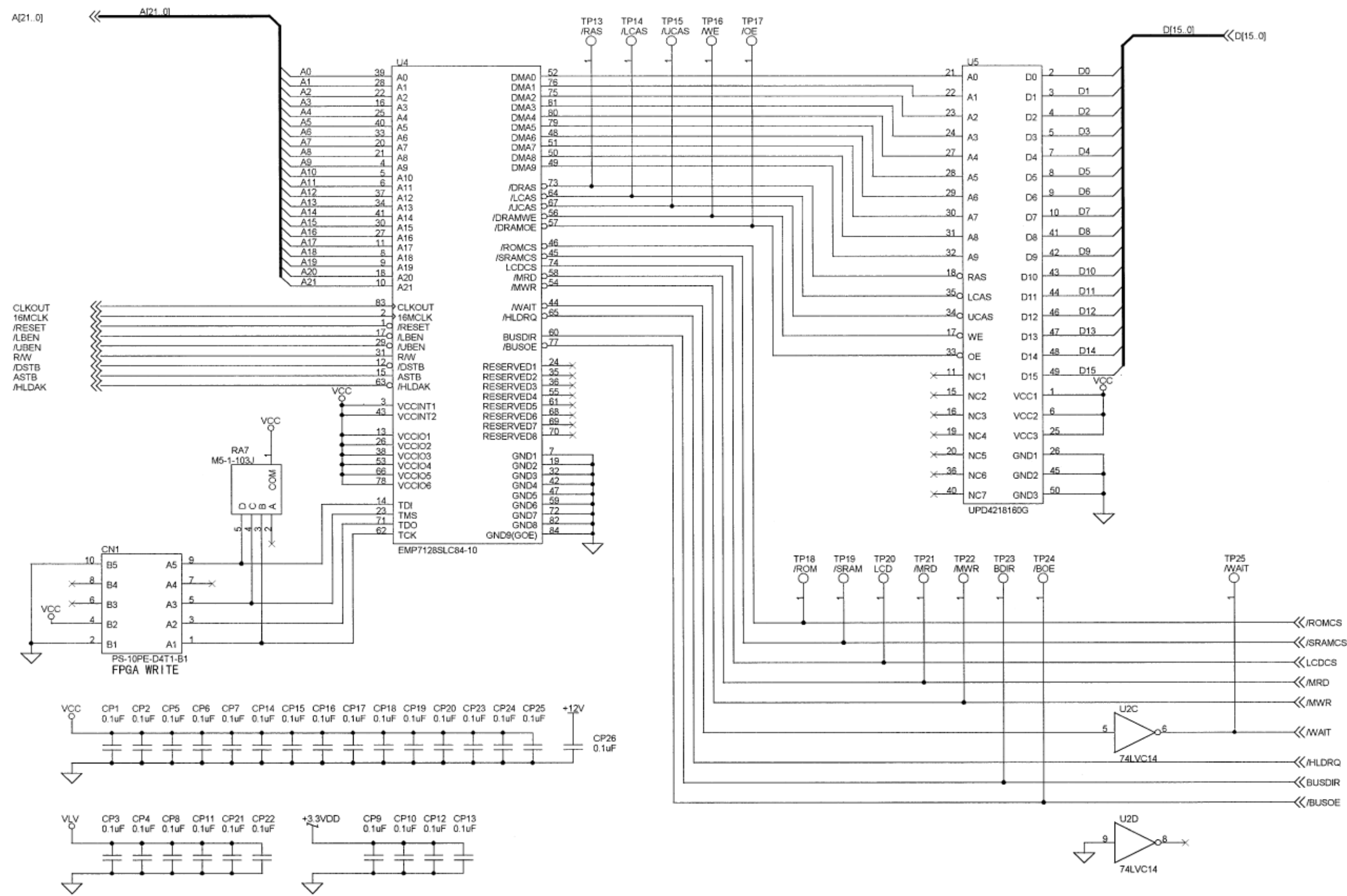


Figure 4-6. Memory

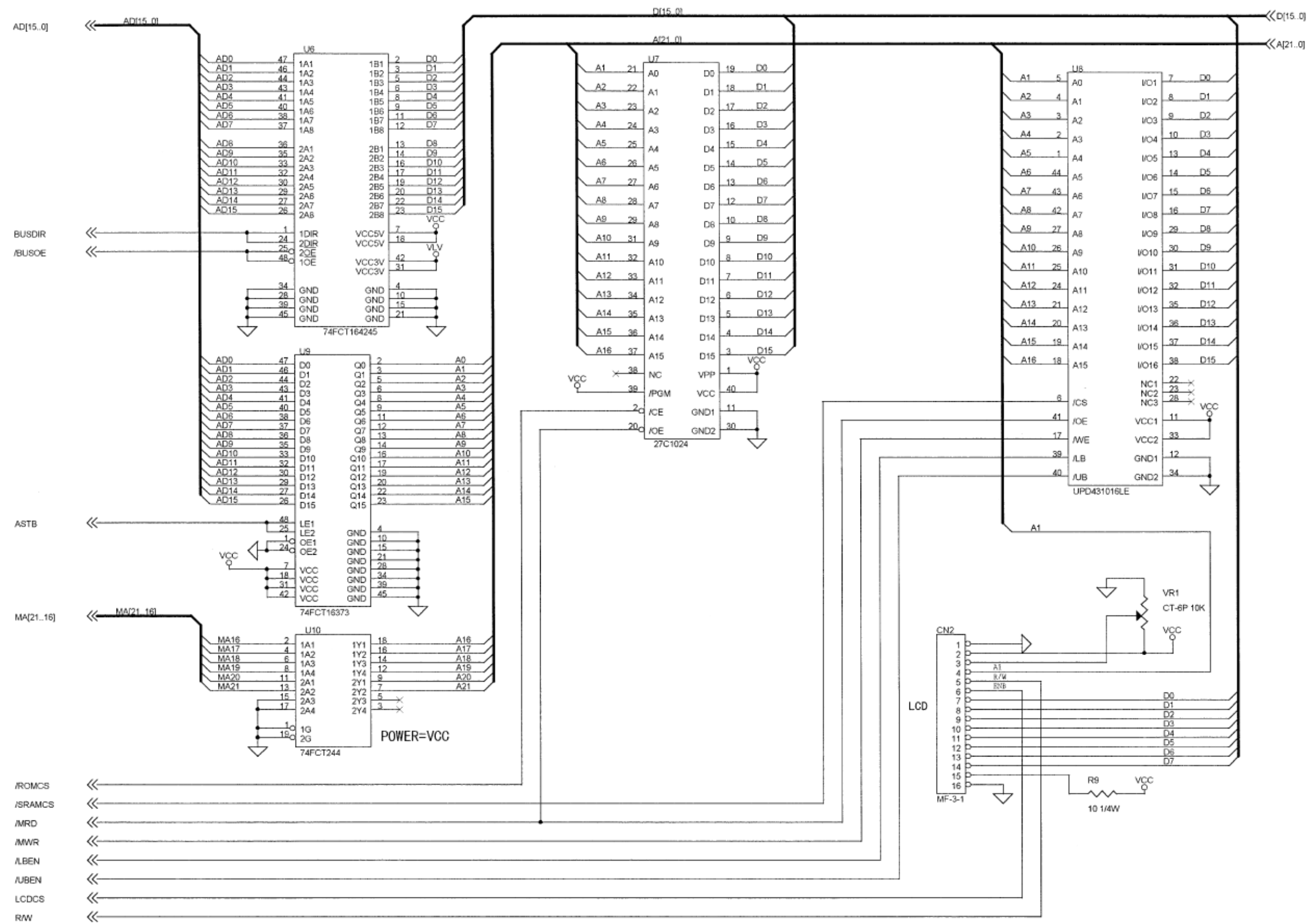


Figure 4-7. I/O Interface

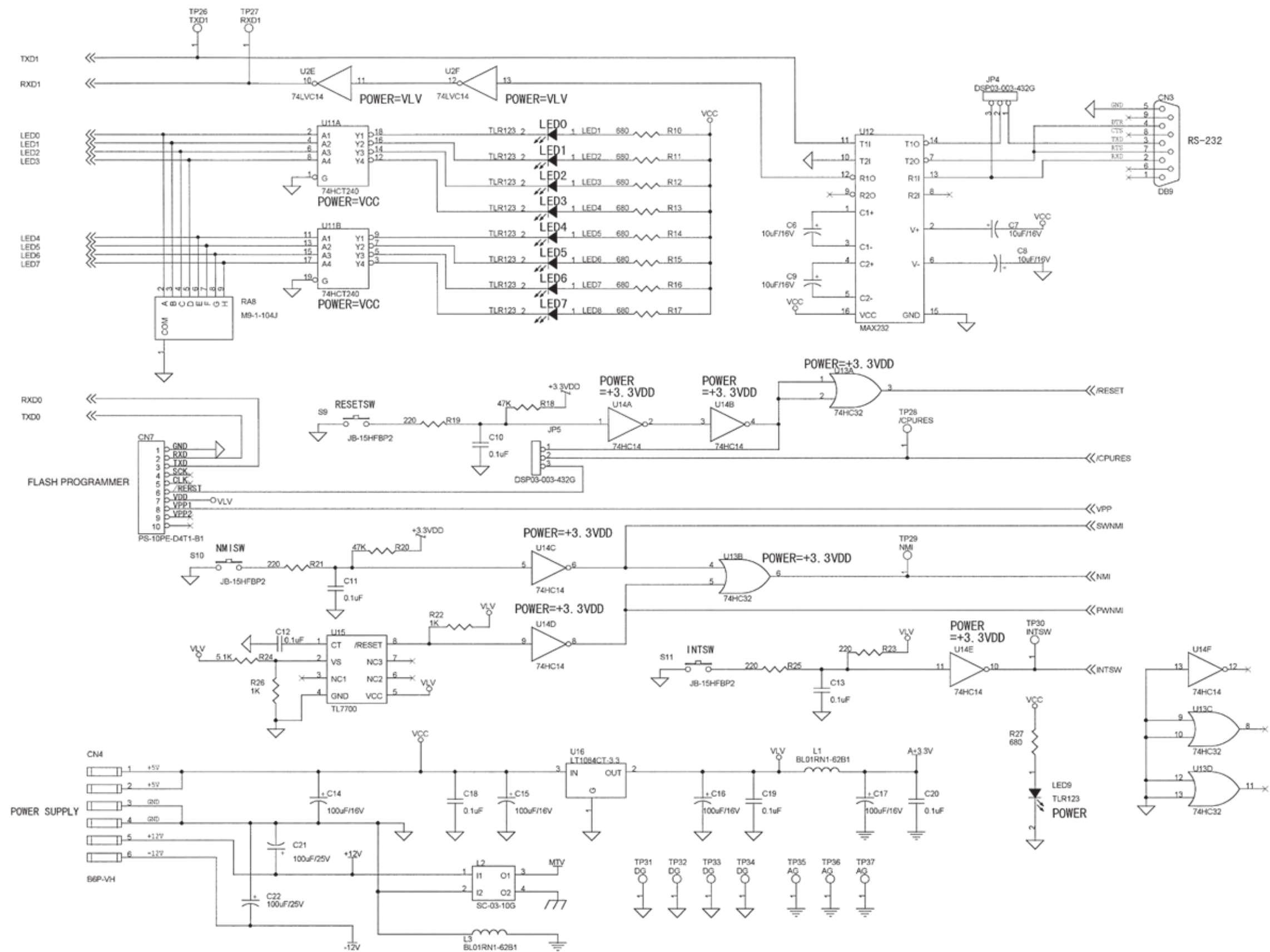


Figure 4-8. Analog I/O

