

Circuit Example (Connection with HN27C4096H-85)

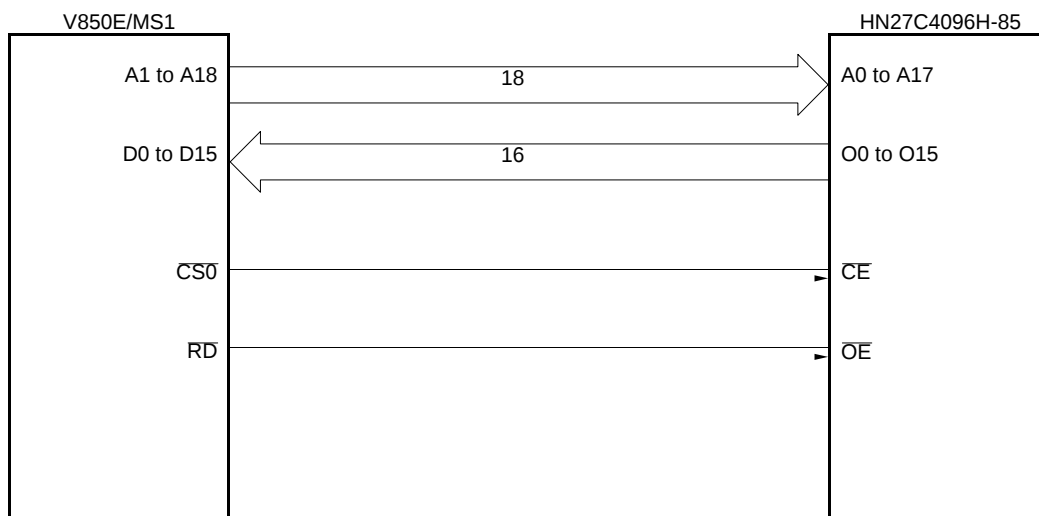
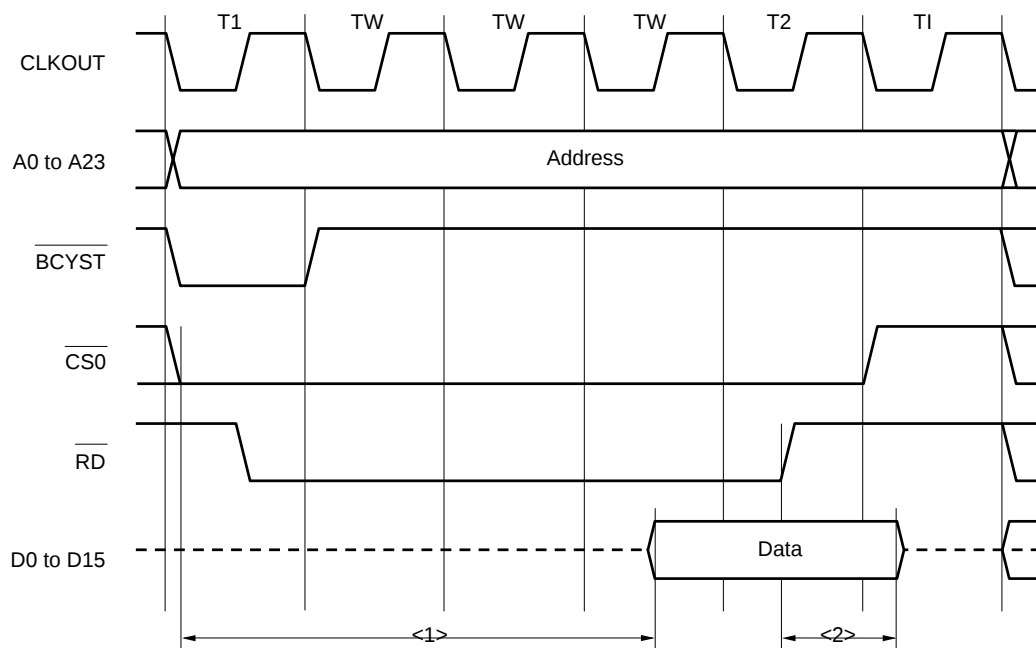


Figure 2-7. Read Operation in HN27C4096H-85



<1> Data output delay time after 27C4096H-85's $\overline{CE}$ signal is active: 85 ns (Max.)

<2> Data output floating delay time after 27C4096H-85's $\overline{OE}$ signal is inactive: 30 ns (Max.)

Remark Broken lines indicate high impedance.